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APPLICANTS

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**** CONTINUING DATA *******
This application is a DIV of 10/138,742 05/03/2002 PAT 6,627,466 *OL*

**** FOREIGN APPLICATIONS *******
none, OL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
**** 01/06/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY OR	SHEETS DRAWING 2	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
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ADDRESS
LSI Logic Corporation
1551 McCarthy Blvd.
Milpitas, CA
95035

TITLE
Method and apparatus for detecting backside contamination during fabrication of a semiconductor wafer

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